

Application No.: 10/707,884

Docket No.: 22040-00028-US

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

Claims 1-3: (Canceled).

4. (Currently amended) A multistage amplifier, comprising:

a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage; and

a ground line connected to said plurality of amplifiers;

wherein substrates held by said plurality of amplifiers are each individually connected to said ground line.

5. (Currently amended) A multistage amplifier, comprising:

a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;

a power source line connected to said plurality of amplifiers; and

a ground line connected to said plurality of amplifiers;

wherein said power source line has a first power source line connected to at least the initial-stage amplifier from among said plurality of amplifiers, and a second power source line commonly connected to the remaining amplifiers except for at least said initial-stage amplifier;

wherein substrates held by said plurality of amplifiers are each individually connected to said ground line.

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6. (Original) The multistage amplifier according to claim 5, wherein said ground line has a first ground line connected to at least said initial-stage amplifier, and a second ground line commonly connected to the remaining amplifiers except for at least said initial-stage amplifier.

Claims 7-11: (Canceled).

12. (Currently amended) An integrated circuit, comprising:

a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;

a ground line connected to said plurality of amplifiers; and

a ground pad connected to said ground line;

wherein substrates held by said plurality of amplifiers are each individually connected to said ground line.

13. (Currently amended) An integrated circuit, comprising:

a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;

a power source line connected to said plurality of amplifiers;

a ground line connected to said plurality of amplifiers;

a power source pad connected to said power source line; and

a ground pad connected to said ground line;

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wherein said power source line has a first power source line connected between at least the initial-stage amplifier from among said plurality of amplifiers and said power source pad, and a second power source line commonly connected between the remaining amplifiers except for at least said initial-stage amplifier and said power source pad;

wherein substrates held by said plurality of amplifiers are each individually connected to said ground line.

14. (Original) The integrated circuit according to claim 13, wherein said ground line has a first ground line connected between at least said initial-stage amplifier and said ground pad, and a second ground line commonly connected between the remaining amplifiers except for at least said initial-stage amplifier and said ground pad.

Claims 15-18: (Canceled).